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Correlative-vector DF Processor WJ-8996-1



The WJ-8996-1 represents the next generation in light-weight, manportable Direction Finding (DF) processors. State-of-the-art design and manufacturing techniques combine to yield a low power, light-weight unit with extraordinary capabilities. The processor contains four complete RF channels to allow modulation-independent acquisition of short duration signals. A correlative-vector DF algorithm is implemented that offers improved performance and antenna flexibility. Previously, such algorithms were only available on much larger systems.

WJ designed the WJ-8996-1 primarily for manportable missions. The mechanical design minimizes weight while maintaining durability.

The WJ-8996-1 functions as a core DF processor. Although the basic unit does not contain a front panel, front-panel operation is easy via a laptop computer or handheld controller.

The unit is extremely reliable and easily maintained. A built-in calibration source constantly monitors the response of the four channels and implements the needed corrections. Also an extensive Built-in Test (BIT) capability notifies the operator or remote host of any detected fault. The fundamental construction of the WJ-8996-1 makes it easy to maintain and service.

Features

- Lightweight/lowpower*
- 20 to 2000 MHz frequency range (tunable to 0.5 MHz)*
- Correlative-vector DF technique for high accuracy/antennaversatility*
- 4 independent RF channels*
- Ruggedization to MIL-STD-810C*
- DSP technology to obtain high processing gain/DF sensitivity*
- RS-232 interface*
- Signal intercept & acquisition capabilities*
- Capability for netted operations*
- CE certification*

HEIGHT	3.5in.(8.89cm)	DEPTH*	16in.(40.64cm)
WIDTH	8.5in.(21.59cm)	WEIGHT**	13lbs(5.89kg)

*with battery cover
**not including battery

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This material provides up-to-date general information on product performance and use. It is not contractual in nature, nor does it provide warranty of any kind.

Functional Description

The WJ-8996-1 DF Processor uses one of the most accurate and versatile DF techniques available. This technique is based on the ability to learn the response of the DF antenna array to received signals over all angles and frequencies. This response is referred to as the database, or the manifold of the antenna array. For each antenna element, a complex voltage vector describes the amplitude and phase of that element. Once the database is accurately established, a Line-of-Bearing (LOB) on a received signal is calculated by measuring the voltage vectors and comparing, or correlating, these vectors to those contained in the database. The point at which the best match or correlation is achieved is taken as the LOB. This DF technique is referred to as Correlative Vector DF.

The Correlative-vector DF technique permits the use of a large range of DF antennas. The WJ-8996-1 processor works with various WJ manportable antenna arrays such as the WJ-9881-X series, the WJ-9887 low-profile series, and the WJ-9896 HF monopole array. In addition, the unit is easily integrated with customer-supplied antenna arrays or DF antennas from other vendors. A user can also configure up to a total of four WJ-8996-1 units into a single network. The operator can use LOB data from the outstations to calculate a fix on transmitter positions.

The following figure shows a functional block diagram of the WJ-8996-1. The unit consists of three primary sections: RF Assembly, DC/DC Options Board, and Digital Board.

The RF Assembly houses four complete RF front ends. These *RF Channels* simultaneously sample the outputs from up to four antenna elements. This information is digitized and used in the DF algorithms. The Local Oscillators (LOs) are coherently split between the RF channels. Direct Digital Synthesis technology improves tuning speed and resolution. A Calibration Channel permits real time calibration of the amplitude and phase differences between the RF Channels. This calibration is accomplished by injecting the same calibration signal at the antenna outputs, and measuring the differences between the channels.

The Digital Board contains the Digital Signal Processing (DSP) Processor (TMS320C31) and the Control Processor (MC68340FE). The operational tasks of the WJ-8996-1 are divided among the various processors in an optimal fashion. The Control Processor manages the overall operation of the unit while the DSP Processor performs the majority of the signal processing functions. The board also contains hardware necessary for netting operations.

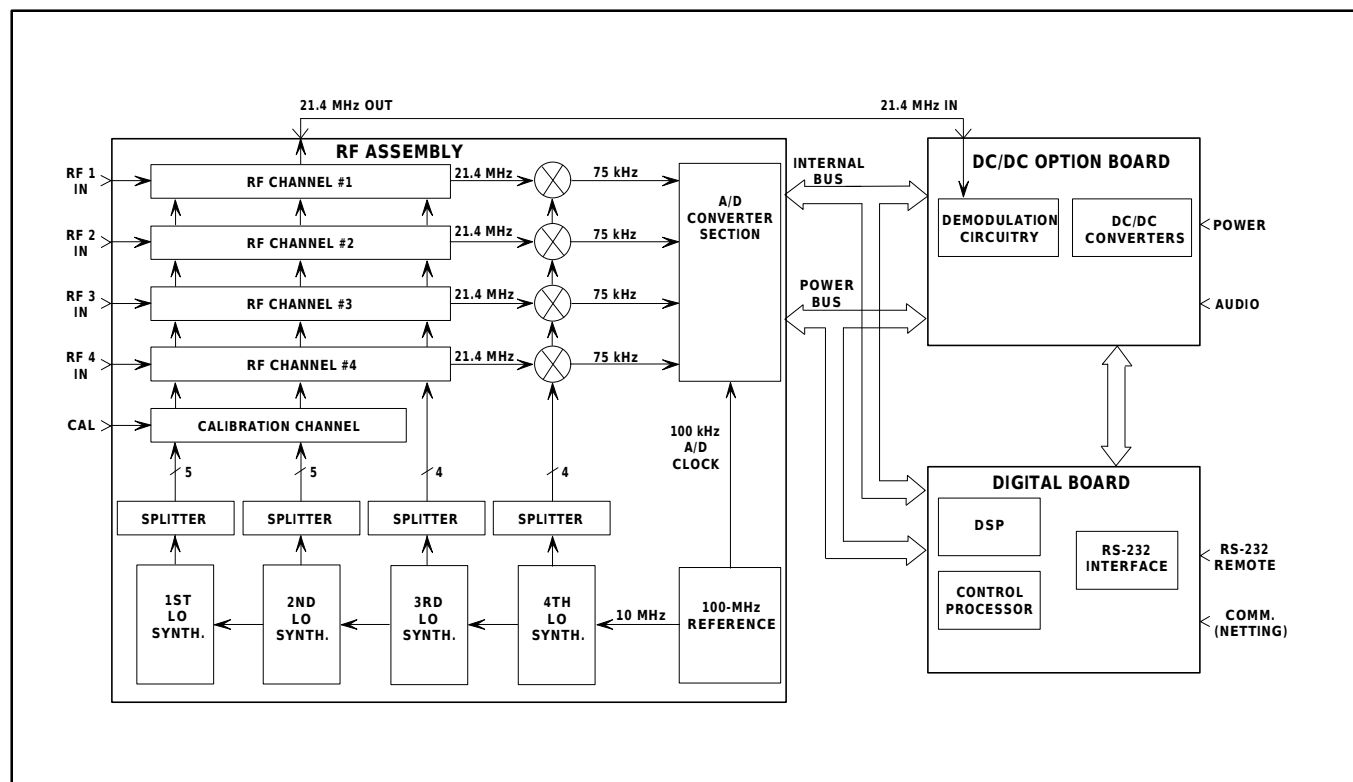
The DC/DC Options Board contains the power supplies and associated filtering. In addition, this board contains demodulation circuitry for AM, FM, CW, LSB, and USB detection modes.

Options and Future Enhancements

The fundamental WJ-8996-1 design easily incorporates a variety of upgrades to increase capabilities and performance. See WJ-8991/SYS data sheet for details of options available. For detailed specifications, contact the factory.

Specifications

SYSTEM (DF processor with typical antenna)	
Frequency Range	20 to 1200 MHz
Accuracy	2 to 5° rms (antenna & site dependent; 3° rms, typical)
Sensitivity	<10 mV/m, typical
Power Consumption	<20 W, typical
PROCESSOR	
Frequency Range	20 to 2000 MHz (Tuning allowed to 0.5 MHz)
Tuning Resolution	100 Hz, synthesized
Demodulation	AM, FM, CW, USB, LSB
Internal Reference Accuracy	±5 parts in 10 ⁻⁶ (0 to 50°C)
Input VSWR	2.0:1, typical
Noise Figure	16 dB (20 to 1200 MHz) max 17 dB (>1200 MHz) max
3rd-order Intercept Point	-20 dBm
Dynamic Range	110 dB
Sampling Rate	100 kHz (12 bits)
Instantaneous Dynamic Range	60 dB, typical
Internally Generated Spurious	< -100 dBm equivalent input, typical
Power Source	11 to 36 Vdc (designed for battery operations)
Battery	BA-5590/U
Control Processor	MC68340
DSP Processor	TMS320C31
Digital Bandwidths (kHz)	25, 12.5, 6.25, 3.125, 1.563, 0.781, 0.391, 0.195
Operating Temperature	-20 to +60°C
Storage Temperature	-40 to +70°C
Shock & Vibration	Designed to MIL-STD-810C
Humidity	95% RH, noncondensing



WJ-8996-1 Functional Block Description

Front-panel Connectors

Connector	Reference Designator	Description
<p>CHAN 1 CHAN 2 CHAN 3 CHAN 4 CAL POWER</p>	<p>J1 J2 J3 J4 J5 J6</p>	<p>Channel 1 input from DF element 1. BNC-50W Channel 2 input from DF element 2. BNC-50W Channel 3 input from DF element 3. BNC-50W Channel 4 input from DF element 4. BNC-50W Calibration signal output. BNC-50W Power input. 6-pin environmental connector - JTL07RE-10-98P 1 - GND 2 - +V 3 - +BATT 4 - GND 5 - +CHARGE 6 - -CHARGE</p>
<p>REMOTE</p>	<p>J7</p>	<p>2 RS-232 serial interfaces - JTL07RE-10-13S</p>
<p>EXTERNAL/CONTROL</p>	<p>J8</p>	<p>Antenna control outputs & external control inputs - JTL07RE-10-13S 1 - CAL 2 - Antenna Control C3 3 - Antenna Control C2 4 - Antenna Control C1 5 - Antenna Control C0 6 - GND 7 - +5V 8 - -5V 9 - Reserved 10 - Reserved 11 - Reserved 12 - Reserved 13 - Reserved</p>
<p>AUDIO</p>	<p>J9</p>	<p>Audio output - GC283 A - GND B - Output C - Push to Talk (PTT) D - Input E - Not used F - Not used</p>
<p>COMM</p>	<p>J10</p>	<p>Communications port - GC283 A - GND B - Input C - Push to Talk (PTT) D - Output E - Not used F - Not used</p>